



Attorney Docket No. 5308-162

Cafe
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Slater, Jr. et al.

U.S. Patent No.: 6,791,119 *B2*

Application Serial No.: 10/057,821

Issued: September 14, 2004

Filed: January 25, 2002

For: *Light Emitting Diodes Including Modifications for Light Extraction*

Date: November 23, 2004

Commissioner for Patents

Attn: Certificate of Correction Branch

P.O. Box 1450

Alexandria, VA 22313-1450

Certificate
DEC 01 2004
of Correction

**REQUEST FOR ENTRY OF CERTIFICATE OF CORRECTION UNDER
35 U.S.C §254 AND 37 C.F.R. §1.322**

Sir:

The Assignee of record for the above-referenced patent hereby requests, pursuant to 35 U.S.C §254 and 37 C.F.R. §1.322, that a Certificate of Correction be issued. This request is made in order to correct the mistakes incurred through the fault of the U.S. Patent and Trademark Office. No fee is believed due. However, the Commissioner is authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

The mistakes appearing in the patent are set forth with corrections on the Certificate of Correction enclosed herewith, with an additional copy thereof and a return post card.

Respectfully submitted,

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Certificate of Mailing under 37 CFR 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Attn: Certificate of Correction Branch, P.O. Box 1450, Alexandria, VA 22313-1450 on November 23, 2004.

Monica L. Croom

DEC 03 2004

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,791,119 *B2*
 DATED : September 14, 2004
 INVENTOR(S) : Slater, Jr. et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Cover Sheet:

Section 56, OTHER DOCUMENTS should include:

U.S. Application Serial No. 60/411,980, filed 09/19/02, *Phosphor-Coated Light Emitting Diodes Including Tapered Sidewalls, and Fabrication Methods*

U.S. Application Serial No. 10/003,331, filed 10/31/01, *Low Temperature Formation of Backside Ohmic Contacts for Vertical Devices*

U.S. Application Serial No. 60/294,445, filed May 30, 2001, *Multi-Quantum Well Light Emitting Diode Structure*

U.S. Application Serial No. 60/294,378, filed May 30, 2001, *Light Emitting Diode Structure With Multi-Quantum Well and Superlattice Structure*

U.S. Application Serial No. 60/294,308, filed May 30, 2001, *Light Emitting Diode Structure With Superlattice Structure*

U.S. Application Serial No. 09/787,189, filed 03/15/01, *Low Temperature Formation of Backside Ohmic Contacts for Vertical Devices*

In the Claims:

Column 29, line 3 should read -- **90.** A light emitting diode according to claim **76** further --

Column 29, line 27 should read -- **96.** A light emitting diode according to claim **35** further --

Column 29, line 34 should read -- **98.** A light emitting diode according to claim **3** wherein --

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PATENT NO. 6,791,119
 No. of additional copies: _____



This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you are required to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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